#### TSPi Plan Summary: Form SUMP

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Name | Hoe You Tien | | | Date | | | 10th December 2014 | | | | |
| Team | .DUEL | | | Instructor | | | Mdm. Nurfauza Jali | | | | |
| Part/Level | Game (Tetris) | | | Cycle | | | 1 & 2 | | | | |
| **Product Size** | | | **Plan** | | | | |  | **Actual** | | |
| Requirements pages (SRS) | | | 10 | | | | |  | 11 | | |
| Other text pages | | |  | | | | |  |  | | |
| High-level design pages (SDS) | | | 10 | | | | |  | 10 | | |
| Detailed design lines | | |  | | | | |  |  | | |
| Base LOC (B) (measured) | | | 1800 | | | | |  | 889 | | |
| Deleted LOC (D) | | |  | | | | |  |  | | |
|  | | | (Estimated) | | | | |  | (Counted) | | |
| Modified LOC (M) | | | 200 | | | | |  | 170 | | |
|  | | | (Estimated) | | | | |  | (Counted) | | |
| Added LOC (A) | | | 800 | | | | |  | 192 | | |
|  | | | (N-M) | | | | |  | (T-B+D-R) | | |
| Reused LOC (R) | | | 800 | | | | |  | 620 | | |
|  | | | (Estimated) | | | | |  | (Counted) | | |
| Total New and Changed LOC (N) | | | 1000 | | | | |  | 362 | | |
|  | | | (Estimated) | | | | |  | (A+M) | | |
| Total LOC (T) | | | 1800 | | | | |  | 889 | | |
|  | | | (N+B-M-D+R) | | | | |  | (Measured) | | |
| Total New Reuse LOC | | |  | | | | |  |  | | |
| Estimated Object LOC (E) | | |  | | | | |  |  | | |
| Upper Prediction Interval (70%) | | |  | | | | |  |  | | |
| Lower Prediction Interval (70%) | | |  | | | | |  |  | | |
| **Time in Phase (hours)** | | **Plan** | | |  | **Actual** | | | |  | **Actual %** |
| Management and Miscellaneous | | 3 | | |  | 3 | | | |  | 0.21 |
| Launch and Strategy | | 3 | | |  | 4 | | | |  | 0.29 |
| Planning | | 36 | | |  | 38 | | | |  | 2.71 |
| Requirements | | 36 | | |  | 38 | | | |  | 2.71 |
| System Test Plan | | 78 | | |  | 80 | | | |  | 5.70 |
| REQ Inspection | | 45 | | |  | 44 | | | |  | 3.14 |
| High-Level Design | | 90 | | |  | 92 | | | |  | 6.56 |
| Integration Test Plan | | 35 | | |  | 24 | | | |  | 1.71 |
| HLD Inspection | | 25 | | |  | 30 | | | |  | 2.14 |
| Detailed Design | | 110 | | |  | 120 | | | |  | 8.55 |
| DLD Review | | 25 | | |  | 30 | | | |  | 2.14 |
| Test Development | | 46 | | |  | 34 | | | |  | 2.42 |
| DLD Inspection | | 67 | | |  | 67 | | | |  | 4.78 |
| Code | | 250 | | |  | 280 | | | |  | 19.96 |
| Code Review | | 200 | | |  | 200 | | | |  | 14.26 |
| Compile | | 50 | | |  | 36 | | | |  | 2.57 |
| Code Inspection | | 24 | | |  | 24 | | | |  | 1.71 |
| Unit Test | | 100 | | |  | 120 | | | |  | 8.55 |
| Build and Integration Test | | 24 | | |  | 26 | | | |  | 1.85 |
| System Test | | 84 | | |  | 88 | | | |  | 6.27 |
| Documentation | | 26 | | |  | 25 | | | |  | 1.77 |
| Total | | 1357 | | |  | 1403 | | | |  | 100 |
| Total Time UPI (70%) | |  | | |  |  | | | |  |  |
| Total Time LPI (70%) | |  | | |  |  | | | |  |  |

**(continued)**

#### TSPi Plan Summary: Form SUMP (continued)

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Defects Injected** | **Plan** |  | **Actual** |  | **Actual %** |
| Management and Miscellaneous | 0 |  | 0 |  | 0 |
| Launch and Strategy | 8 |  | 5 |  | 6.76 |
| Planning | 12 |  | 10 |  | 13.51 |
| Requirements | 2 |  | 0 |  | 0 |
| System Test Plan | 0 |  | 0 |  | 0 |
| REQ Inspection | 6 |  | 5 |  | 6.76 |
| High-Level Design | 0 |  | 0 |  | 0 |
| Integration Test Plan | 6 |  | 3 |  | 4.05 |
| HLD Inspection | 6 |  | 2 |  | 2.70 |
| Detailed Design | 5 |  | 4 |  | 5.40 |
| DLD Review | 3 |  | 3 |  | 4.05 |
| Test Development | 4 |  | 4 |  | 5.40 |
| DLD Inspection | 5 |  | 5 |  | 6.76 |
| Code | 25 |  | 32 |  | 43.24 |
| Code Review | 5 |  | 2 |  | 2.70 |
| Compile | 3 |  | 2 |  | 2.70 |
| Code Inspection | 5 |  | 3 |  | 4.05 |
| Unit Test | 5 |  | 4 |  | 5.40 |
| Build and Integration Test | 4 |  | 2 |  | 2.70 |
| System Test | 0 |  | 0 |  | 0 |
| Documentation | 0 |  | 0 |  | 0 |
| Total Development | 104 |  | 74 |  |  |
| **Defects Removed** | **Plan** |  | **Actual** |  | **Actual %** |
| Management and Miscellaneous | 0 |  | 0 |  | 0 |
| Launch and Strategy | 4 |  | 2 |  | 2.94 |
| Planning | 0 |  | 0 |  | 0 |
| Requirements | 8 |  | 7 |  | 10.29 |
| System Test Plan | 0 |  | 0 |  | 0 |
| REQ Inspection | 12 |  | 10 |  | 14.71 |
| High-Level Design | 0 |  | 0 |  | 0 |
| Integration Test Plan | 6 |  | 4 |  | 5.88 |
| HLD Inspection | 0 |  | 0 |  | 0 |
| Detailed Design | 5 |  | 4 |  | 5.88 |
| DLD Review | 3 |  | 3 |  | 4.41 |
| Test Development | 4 |  | 4 |  | 5.88 |
| DLD Inspection | 5 |  | 5 |  | 8.82 |
| Code | 25 |  | 21 |  | 30.88 |
| Code Review | 0 |  | 0 |  | 0 |
| Compile | 3 |  | 2 |  | 2.94 |
| Code Inspection | 2 |  | 2 |  | 2.04 |
| Unit Test | 0 |  | 0 |  | 0 |
| Build and Integration Test | 5 |  | 4 |  | 5.88 |
| System Test | 0 |  | 0 |  | 0 |
| Documentation | 0 |  | 0 |  | 0 |
| Total Development | 82 |  | 68 |  |  |